



16/G
Jeresa
10-5-00

1 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2 Application Serial No. 09/059,644
3 Filing Date April 13, 1998
4 Inventor Pai-Hung Pan
5 Assignee Micron Technology, Inc.
6 Group Art Unit 2822
Examiner M. Trinh
7 Attorney's Docket No. MI22-898
Title: Semiconductor Processing Methods of Forming a Conductive
8 Gate and Line

9 **RESPONSE TO JUNE 20, 2000 OFFICE ACTION**

10 To: Assistant Commissioner for Patents
11 Washington, D.C. 20231
12
13 From: Bernard Berman (Tel. 509-624-4276; Fax 509-838-3424)
14 Wells, St. John, Roberts, Gregory & Matkin P.S.
15 601 W. First Avenue, Suite 1300
16 Spokane, WA 99201-3828
17

18 Sir:

19 Responsive to the Office Action dated June 20, 2000, applicant
20 requests reconsideration of the above referenced application in view of
21 the amendments and remarks that follow:
22
23

RECEIVED
JUL 4 2000
TECHNOLOGY CENTER 2800